BITS Pilani Dubai Campus, Dubai, UAE

Semester-wise pattern for Students Admitted to Higher Degree Program in the Second Semester (Operative from I Sem. Academic Year 2011-12 onwards; Ref.:- Base charts approved by Senate 159 and Senate 160)

M.E. Microelectronics

Year	I Semester		U		II Semester @			U	Summer Term U			
				1	MEL	G631	Physics and Modeling of		MEL	G611	IC Fabrication Technology	5
							Microelectronic Devices	5				
I				1	MEL	G642	VLSI Architectures	4			Elective	*
							Elective	*				
								12				8
								(min.)				(min.)
	BITS G540	Research Practice \$	4	ľ	MEL	G632	Analog IC Design	5	BITS	G629T	Dissertation	5
II	MEL G621	VLSI Design	5				Elective	*			Elective	*
		Elective	*				Elective	*				
			12					11				8
			(min.)					(min.)				(min.)
III	BITS G629T	Dissertation	11									
			11									
			(min.)									

^{*} Minimum 3 Units each Elective:

Pool of Electives (Any six from the following)

Elective offerings are subject to availability of (i) faculty who can offer the elective and (ii) the minimum viable no. of students opting for the elective

- 1. CS G553 Reconfigurable Computing 5
- 2. CS G562 Advanced Architecture and Performance Evaluation 3 2 5
- 3. EEE C415 Digital Signal Processing 3 1 4
- 4. EEE G510 RF Microelectronics 5
- 5. EEE G512 Embedded System Design 3 1 4
- 6. EEE C415 Advanced Digital Signal Processing 3 1 4
- 7. EEE G626 Hardware Software Codesign 4

- 8. MEL G512 Optoelectronic Devices, Circuits and Systems 3 2 5
- 9. MEL G612 Integrated Electronic System Design 2 2 4
- 10. MEL G623 Advanced VLSI Design 5
- 11. MEL G625 Advanced Analog and Mixed Signal Design 5
- 12. MEL G626 VLSI Test and Testability 5
- 13. MEL G641 CAD for IC Design 5

Note:

- 1. Semester wise pattern given above is as suggested by the appropriate Senate-appointed committee, subject to change if the situation warrants.
- 2. Pool of Electives listed above are currently approved by Senate, subject to change if the situation warrants
- 3. Upon comparing the student's input degree curriculum with BITS degree curriculum, if a student is found to be inadequately prepared, one or more Deficiency courses may be prescribed and added to the courses of the Semester wise pattern (Chart) given above.

[@] required to register: BITS F437 Technical Communication (3 0 3), unless he/she clears a diagnostic test specially designed for the same.

^{\$:} New Course; # Modified course;